

2019 IEEE Technical Meeting on
Reliable, Safe, Secure, and Time-Deterministic Intelligent Systems

December 6th, 2019

09:30-10:00 Opening Session

Welcome Message from the IEEE Computer Society President, IEEE Digital Reality Initiative Project on Reliable, Safe, Secure and Time Deterministic Intelligent Systems
Co-Chair: Cecilia Metra

Welcome Message from the IEEE Computer Society Vice-President for Standards, IEEE Digital Reality Initiative Project on Reliable, Safe, Secure and Time Deterministic Intelligent Systems Co-Chair: Riccardo Mariani

Welcome Message from the School of Engineering of the University of Bologna
President: Davide Moro

Welcome Message from the IEEE Italy Entrepreneurship Committee Chair: Vincenzo Piuri

Keynote Speakers:

10:00-10:25 Keynote: Roberto Saracco, Chair IEEE Digital Reality Initiative

10:25-10:50 Keynote: Nineta Polemi, Programme Manager - Policy Officer at European Commission - Cyber Security, Technologies and Capacity Building

10:50-11:15 Keynote: Konstantinos Karachalios, IEEE Standards Association Managing Director

11:15-11:30 Coffee Break

11:30-11:55 Keynote: Riccardo Mariani, Industry Safety Vice-President Nvidia

11:55-12:20 Keynote: Yervant Zorian, Synopsys Fellow and Chief Architect, President of Synopsys Armenia

12:20-12:45 Keynote: Ranieri Niccoli, Industrial Director Lamborghini

12:45-13:10 Keynote: Mauro Pipponzi, Functional Safety Methodology Architect, Intel

13:10-14:30 Lunch

14:30-16:15 Panel: Challenges for Reliable, Safe, Secure and Time Deterministic Intelligent Systems

Moderator: Cecilia Metra

Panelists:

IP View: Pete Harrod, Director of Functional Safety, ARM

EDA View: Meirav Nitzan, Program Mgmt Director, Functional Safety Solutions,
Verification, Synopsys

System View: Filippo Brenna, Sr. Solution Architect, NVIDIA Italy

Enrico Sampò, Senior Vehicle Dynamicist, Ferrari

Diego Marino, Engineering Director Italy, Bombardier

Bruno Meyer, Senior Executive, Reseau de Transport d'Electricite (RTE)

Lorenzo Chiarion, Head of Manufacturing Engineering , Lamborghini

Technology View: Jyotika Athavale, Principal Engineer, Senior Functional Safety and
RAS Architect, Intel

SoC View: Arcangelo Bruna, Manager, STMicroelectronics

5G communication View: Ritu Sethi, Software Architect, Intel

Security view: Sohrab Aftabjahani, Staff Security Researcher, Intel

16:15-16:30 Coffee Break

16:30–16:45 Hermann Brand, European Standards Affairs Director

16:45-18:30 Working Session

16:45-17:00 Introduction of meeting working document: Riccardo Mariani, IEEE Digital
Reality Initiative Project on Reliable, Safe , Secure and Time Deterministic Intelligent
Systems Co-Chair

17:00-18:00 Working session: 4 teams will be formed on the 4 topics of the meeting,
Reliability, Safety, Security and Time Determinism. Each team will have 1 hour
of time to review the meeting working document and propose changes.

18:00-18:10 Message from the Deputy Rector of the University of Bologna: Mirko Degli
Esposti

18:10-18:30 Report of the four teams.

18:30-18:40 Summary and next steps on the document: Riccardo Mariani

18:40-18:50 – Closing Message from the IEEE Digital Reality Initiative Project on Reliable, Safe,
Secure and Time Deterministic Intelligent Systems Co-Chairs: Riccardo Mariani,
Cecilia Metra